

Low-Resistance Ohmic Contacts to Digital Alloys of n-AlGa_N/AlN

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Abstract—Low contact resistance to digital alloys of n-type AlGa_N/AlN with high average Al concentration is described. Low-energy electron diffraction was used to evaluate surface precleaning with HCl and buffered HF. The contact metallization consisting of a stack of Ti/Al/Ti/Au, 20/100/45/60 nm in thickness, was e-beam deposited and etch-patterned. The lowest specific contact resistance of $5.6 \times 10^{-5} \Omega \cdot \text{cm}^2$ was obtained after annealing in N₂ ambient at 700 °C.

Index Terms—AlGa_N, Ga_N, light-emitting diode, ohmic contact, superlattices.

I. INTRODUCTION

THE AlGa_N-based optoelectronic devices operating in the ultraviolet spectral range of 250–300 nm require large bandgap cladding and contact layers. Short period superlattices of AlGa_N/AlN, also known as digital alloys, can be used instead of random alloys but very little is known about formation of Ohmic contacts to them. Preparation of low-resistance Ohmic contacts to AlGa_N, especially with high AlN content, is considered quite difficult. Contacts based on Ti/Al/Ti/Au are used on n-type Ga_N [1]–[8] and AlGa_N [9]. The specific contact resistivity of 3×10^{-6} and $6 \times 10^{-5} \Omega \cdot \text{cm}^2$ was demonstrated for Ga_N [3] and Al_{0.6}Ga_{0.4}N [9], respectively. Recently, superior results obtained with V-based metallization were reported [10].

In this letter, we describe the use of Ti/Al/Ti/Au metallization to digital alloys of AlGa_N/AlN with an average AlN content of 66%. This range of compositions is used in deep UV LED's operating near 280 nm. Digital alloys of AlGa_N/AlN with an average composition between 0.55 and 0.75 can be prepared without changing the composition of the AlGa_N well material [11]. Since the surface layer in these alloys is always AlGa_N, it is expected that the fabrication of Ohmic contacts to alloys would be relatively insensitive to the average content of AlN. This is indeed the experimental finding and the Ohmic contact described here can be used for a wide range of digital alloy compositions.

II. EXPERIMENTS

Digital alloy samples of Al_xGa_{1-x}N/AlN used in our experiments were grown by gas source molecular beam epitaxy

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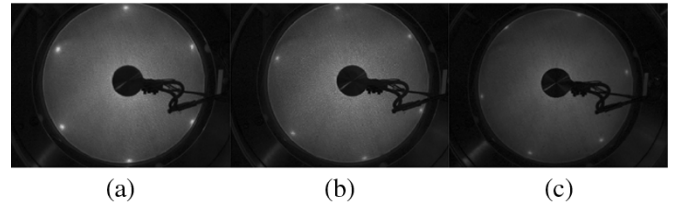


Fig. 1. LEED images of AlGa_N surface at 47 V after cleaned with BHF only (a) and HCl only (b) and HCl and BHF together (c) for 10 min each.

applications in light emitting diodes [11]–[15]. Samples consist of 200 periods of 0.9-nm-thick barriers of AlN and 0.5-nm-thick wells of AlGa_N. The Al fraction in the wells is ~ 0.07 , resulting in the average AlN content of $x = 0.66$. The layer was doped with Si to the average level of $2 \times 10^{19} \text{ cm}^{-3}$, as determined by secondary ion mass spectrometry. All samples were terminated with the well material, i.e., Al_{0.07}Ga_{0.93}N. The effective bandgap of these layers, determined by room temperature cathodoluminescence and optical reflectivity measurements, was found to be $\sim 5.10 \text{ eV}$ [11], [13], corresponding to the average AlN content of $x = 0.66$. The average AlN content, as well as the well and barrier thickness, were independently determined by X-ray diffraction measurements.

Surface cleaning is a critical procedure in achieving low contact resistance. The dominant contaminants on AlGa_N are known to be oxygen and carbon. Oxygen is especially troublesome on layers with high AlN content since Al-oxides act as high-resistivity barriers in the contact. Surface cleaning of Ga_N and AlGa_N with a range of acids, including HCl, HF, buffered HF (BHF), and bases such as KOH, has been reported by various groups but no single treatment has been found superior [16]–[21]. The cleaning process itself can result in surface contamination [16] and sensitive method of determining the surface condition is needed. We use low-energy electron diffraction (LEED), a standard high vacuum diffraction technique [22], to evaluate the effectiveness of different etchants. The surfaces of three samples, cleaved from a single wafer, were etched in room temperature HCl, BHF, and HCl and BHF together, for 10 min each. After etching and DI water rinse the samples were loaded into a LEED chamber. All three samples show well-defined diffraction spots due to the reciprocal lattice structure of the digital alloy, as shown in Fig. 1. The sample etched in BHF, Fig. 1(a), exhibited the brightest and best defined diffraction pattern, indicative of the highest surface quality. Diffraction pattern in this sample could be seen at a bias voltage as low as 34 V. The surface etched in HCl, shown in Fig. 1(b), is of lower quality, as judged by LEED images. A combination of HCL and BHF, shown in Fig. 1(c), was yet less effective. We

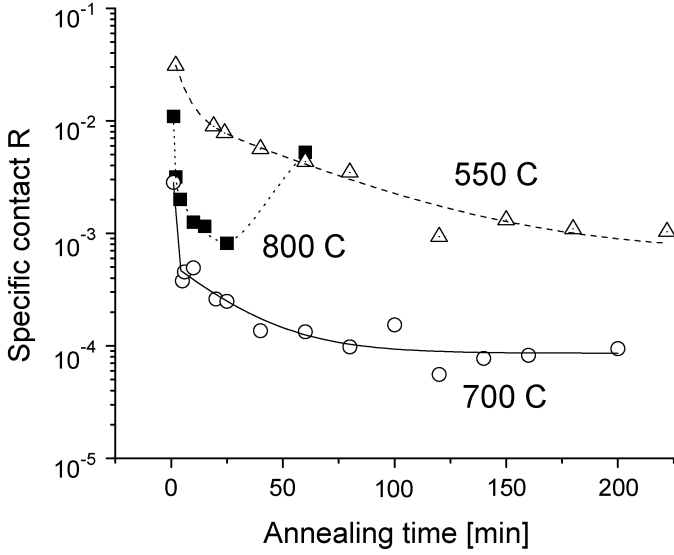


Fig. 2. Specific contact resistance versus annealing time at different temperature.

have also examined surface cleaning of GaN with LEED and found HCl to be slightly more effective than HF or BHF. Our study of cleaning of AlGaIn and GaN surfaces are consistent with previous Auger studies of AlN and GaN [16].

After surface etching each sample was loaded into an e-gun evaporator chamber for deposition of contact metals. A full surface layer of Ti(20 nm)/Al(100 nm)/Ti(45 nm)/Au(60 nm) was deposited at a base pressure below 2×10^{-7} torr [2], [23]–[25]. The metal was patterned using S-1813 photoresist (Shipley) and etched to produce a pattern for circular transmission line method (TLM) measurements. Metal liftoff technique was not used in this experiment since it does not yield reproducible results.

Samples with TLM patterns were furnace annealed for a period of 5–10 min in flowing nitrogen and air, and measured. Longer period anneals, up to 220 min at 550 °C (sample 1), 200 min at 700 °C (sample 2), and 60 min at 800 °C (sample 3) were carried out to evaluate metal-semiconductor interactions. In lower temperature anneals under nitrogen, below 700 °C, the specific contact resistance decreased rapidly during the first 5–10 min, as shown in Figs. 2 and 3. Longer anneals resulted in a slow decrease in contact resistance and eventual saturation. In anneals at 800 °C specific contact resistance reaches a minimum in about 25 min and then it increases. The specific contact resistance of $10^{-4} \Omega \cdot \text{cm}^2$ was obtained after anneal at 700 °C for periods of 40 min and longer. The lowest contact resistance obtained in this letter was $5.6 \times 10^{-5} \Omega \cdot \text{cm}^2$, also reached in anneals at 700 °C. This annealing temperature does not change the average composition of digital alloys, as determined by optical measurements.

We observe large differences in the annealing behavior in nitrogen and air. Fig. 3 plots the specific contact resistance, normalized to resistance after 1 min anneal, as a function of annealing time at 700 °C in nitrogen and air ambient. Annealing in nitrogen results in significant improvement in the specific contact resistance compared to samples annealed in air. In fact, longer term anneals in air, over 80 min, result in significant degradation of the contact resistance as shown in Fig. 3. We at-

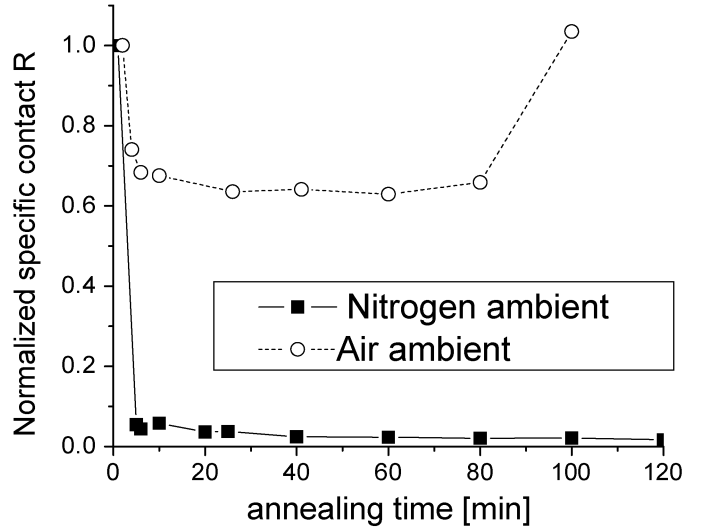


Fig. 3. Normalized specific contact resistance versus annealing time at 700 °C for different ambients.

tribute this to the formation of an oxide barrier, as discussed below.

Samples with thinner Ti layer reached minimum specific contact resistance after shorter annealing times. For instance, when the thickness of the first Ti layer, deposited at the surface of the superlattice, was reduced from 20 to 10 nm, the minimum specific contact resistance was reached in half the annealing time. The thickness of this layer does not alter the contact resistance.

III. RESULT AND DISCUSSION

The metal sequence of the Ti/Al/Ti/Au metallization, and the individual layer thickness, was carefully designed. The first Ti layer is used to assure adhesion and to react residual surface oxides by forming TiO_2 (with the bandgap of 3.05 eV), an oxide with the bandgap smaller than that of GaN (3.5 eV), Ga_2O_3 (4.4 eV) and Al_2O_3 (8.8 eV) [26]–[29]. The Ti layer can also react with nitrogen in AlGaIn and form TiN [4]–[7], [25], [30], [31], increasing the effective carrier concentration near the surface. Formation of Ohmic contacts with Ti layer alone, via formation of TiN, requires high annealing temperatures, 900 °C and above, [2], [30]. Interaction of Al with nitrogen in AlGaIn occurs at lower temperatures and result in formation of Ohmic contact to AlGaIn [5], [29], [30], and that is the reason for incorporating Al into the contact. In the absence of interfacial Ti, Al reacts with surface oxides forming insulating Al_2O_3 . With the Ti/Al combination low-resistance contact is obtained after Al diffuses through the surface Ti layer. The anneal time is reduced for thinner Ti layers, as observed in our study. At moderate annealing temperature specific contact resistance decreases as this diffusion process takes place and then saturates. The second Ti layer, deposited over the Al layer, is used to form an oxygen barrier [7], [32], [33]. During annealing, the Al/Ti interaction results in formation of TiAl_3 which acts as an oxygen diffusion barrier [23], [24], [27], inhibiting formation of surface oxides of Al or Ti. [2] Even with this Ti barrier annealing in nitrogen produces lower contact resistance than annealing in air, as confirmed in our experiments. Another argument for annealing in

nitrogen was provided by studies of Ti/Al couples showing oxidation of surface segregated Ti and formation of TiO_2 in 600 °C to 700 °C anneals [33].

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