

# Derek Allen Johnston

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## Education

### **Master of Science in Electrical Engineering**

Texas Tech University

*Program for Semiconductor Product Engineering*

**May 2016**

### **Bachelor of Science in Electrical Engineering**

Texas Tech University

**May 2013**

## Teaching

### **Instructor**

*Texas Tech University*

**2019 – Present**

Instructor of record for the following courses:

- ENGR 1330 – Computational Thinking with Data Science
- ECE 2372 – Modern Digital System Design
- ECE 3304 – Discrete-Time Signals and Systems
- ECE 3306 – Electric Circuits II
- ECE 3331, 3332, and 4334 – Project Laboratory I, II, and V
- ECE 3341 – Electromagnetic Theory I
- ECE 4365 – Parametric and Functional Device Testing
- ECE 4366 – Testing of Digital Systems

### **Graduate Part-Time Instructor**

*Texas Tech University*

**2013 – 2016**

### **Supplemental Instruction Leader**

*Support Ops for Academic Retention, Texas Tech University*

**2012 – 2013**

## Service

### **Interim Hub Director**

*West Texas BEST Robotics*

**2021 – Present**

### **Faculty Advisor**

*Institute of Electrical and Electronics Engineering, Student Section*

**2019 – Present**

### **Laboratory Manager**

*Program for Semiconductor Product Engineering*

**2019 – Present**

## Publications and Conference Presentations

- D. Johnston, B. Nutter and R. Gale, "IC Discrimination via Novel S-Parameter Measurement Technique," 2020 IEEE International Instrumentation and Measurement Technology Conference (I2MTC), 2020, pp. 1-5, doi: 10.1109/I2MTC43012.2020.9129222.
- D. Johnston and R. Gale, "Segmenting Semiconductor Test Curriculum into a Course Sequence" Co-Speaker, NIWeek 2018, May 2018